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Subject: How to represent Line Continuation on railML  
Posted by [Fabiana Diotallevi](#) on Mon, 08 Jul 2019 16:02:00 GMT  
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Dear all,

if I want to export in railML 3.1 the schematic of a station that has several "real" bufferstops, and also some "fake" bufferstops representing the line borders (see image below), how can I export the fake ones?

I need to export them beacuse they define the borders of the tvdSection for the interlocking schema.

Any ideas?

Thanks in advance,  
f.

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#### File Attachments

1) [bufferstops.JPG](#), downloaded 555 times

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